

Attorney Docket No.: 0254-094-CIP4/CIP-MB

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Stacked Module Systems and Methods

Application No.: TBA

Filing Date: 04/20/2004

Inventor: Julian Partridge & James D. Wehrly, Jr.

Assignee: Staktek Group, L.P.

Examiner: TBA

Group: TBA

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Pursuant to the guidelines for Information Disclosure Statements, 37 C.F.R. §§ 1.97-1.98, attached hereto is PTO Form 1449 (five sheets) with references listed for consideration by the Office.

In accordance with 37 C.F.R. § 1.98(a), copies of the listed written references are being provided.

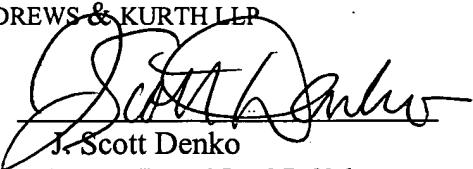
In accordance with 37 C.F.R. §§ 1.97(g)-(h), the filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b) or prior art under 35 U.S.C. §§ 102 or 103.

This Information Disclosure Statement is being filed before receipt of the first Office action, so no fee is believed to be due. The Commissioner is hereby authorized to charge any fees which may be required in connection with the filing, or to credit any overpayment to Deposit Account No. 50-0897, upon which account the undersigned has the authority to sign. A **duplicate copy of Form 1449 is enclosed.**

Respectfully submitted,

April 20, 2004  
Date

ANDREWS & KURTH LLP

By: 

J. Scott Denko  
Patent Reg. No. 37,606  
111 Congress Ave., Suite 1700  
Austin, Texas 78701  
(512) 320-9200 Telephone  
(512) 320-9292 Facsimile

Form PTO-1449 (Modified) (Rev. 11/92)		U.S. Dept. of Commerce Patent and Trademark Office	ATTY. DOCKET NO.	SERIAL NO.
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)  (37 CFR 1.98(b))		254-094-CIP4/CIP-MB		
		APPLICANT	Julian Partridge & James D. Wehrly, Jr.	
		FILING DATE	GROUP	
		4/20/2004		

U.S. PATENT DOCUMENTS						
*Examiner Initial	Patent Number		Patentee	Class	Subclass	Filing Date (if appropriate)
	5,428,190	6/27/1995	Stopperan	174/261		7/2/1993
	6,097,087	8/1/2000	Farnworth, Warren M. et al	257/698		10/31/1997
	6,323,060B1	11/27/2001	Isaak	438/109		5/5/1999
	6,351,029B1	2/26/2002	Isaak	257/688		5/19/2000
	6,368,896B2	4/9/2002	Farnworth, Warren M. et al	438/113		4/23/1999
	6,410,857B1	6/25/2002	Gonya	174/254		8/13/2001
	6,426,240B2	7/30/2002	Isaak	438/106		6/25/2001
	6,426,549B1	7/30/2002	Isaak, Harlan R.	257/686		11/3/2000
	6,444,921B1	9/3/2002	Wang, et al	174/260		2/3/2000
	6,465,877B1	10/15/2002	Farnworth, Warren M. et al	257/668		3/27/2000
	6,514,793B2	2/4/2003	Isaak	438/109		2/4/2003

**FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION**

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	58-96756(A)	06/08/1983	Japan			Partial	
	58-112348(A)	07/04/1983	Japan			Partial	
	60-254762	12/1985	Japan			Partial	
	EP 0 298 211 A#	01/11/1989	European			X	

**OTHER DOCUMENTS**

(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

	Dense-Pac Microsystems, Breaking Space Barriers, 3-D Technology 1993
	Dense-Pac Microsystems, DPS512X16A3, Ceramic 512K X 16 CMOS SRAM Module
	"Design Techniques for Ball Grid Arrays," William R. Newberry, Xynetix Design Systems, Inc.

EXAMINER	DATE CONSIDERED
----------	-----------------

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 (Modified) (Rev. 11/92)		U.S. Dept. of Commerce Patent and Trademark Office	ATTY. DOCKET NO.	SERIAL NO.
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)  (37 CFR 1.98(b))		254-094-CIP4/CIP-MB		
		APPLICANT	Julian Partridge & James D. Wehrly, Jr.	
		FILING DATE	GROUP	
		4/20/2004		

U.S. PATENT DOCUMENTS						
*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
	6,262,895 B1	7/17/2001	Forthum, John A.	361/749		1/13/2000
	5,239,198	8/24/1993	Lin et al.	257/693		7/2/1992
	6,028,365	2/22/2000	Akram et al.	257/778		3/30/1998
	6,433,418	8/13/2002	Fujisawa et al.	257/696		4/29/1999
	6,208,521 B1	3/27/2001	Nakatsuka, Yasuo	361/749		5/19/1998
	6,265,660 B1	7/24/2001	Tandy, Patrick W.	174/52.4		8/18/2000
	6,300,679 B1	10/9/2001	Mukerji et al.	257/738		6/1/1998
	6,462,412 B2	10/8/2002	Kamei et al	257/723		1/17/2001
	6,465,893 B1	10/15/2002	Khandros et al.	257/777		10/19/2000
	6,486,544 B1	11/26/2002	Hashimoto, Nobuaki	257/686		9/3/1999
	6,560,117 B2	5/6/2003	Moon, Ow Chee	361/749		8/30/2001
	6,590,282 B1	7/8/2003	Wang et al.	257/686		4/12/2002
	6,600,222 B1	7/29/2003	Levardo, Melvin N.	257/686		7/17/2002

**FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION**

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	62-230027(A)	10/08/1987	Japan			Partial	

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
	"Chip Scale Packaging and Redistribution," Paul A. Magill, Glenn A. Rinne, J. Daniel Mis, Wayne C. Machon, Joseph W. Baggs, Unitive Electronics Inc.
	"3D Interconnection for Ultra-Dense Multichip Modules," Christian VAL, Thomson-CSF DCS Computer Division, Thierry Lemoine, Thomson-CSF RCM Radar Countermeasures Division
	"High Density Memory Packaging Technology High Speed Imaging Applications," Dean Frew, Texas Instruments Incorporated
	Teresa Technologies, Inc. – Semiconductor Intellectual Property, Chip Scale Packaging – Website pages (3)

EXAMINER	DATE CONSIDERED
----------	-----------------

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 (Modified) (Rev. 11/92)		U.S. Dept. of Commerce Patent and Trademark Office	ATTY. DOCKET NO.	SERIAL NO.
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)  (37 CFR 1.98(b))		254-094-CIP4/CIP-MB		
		APPLICANT	Julian Partridge & James D. Wehrly, Jr.	
		FILING DATE	GROUP	
		4/20/2004		

U.S. PATENT DOCUMENTS						
*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
	6,620,651B2	9/16/2003	He et al.	438/113		10/23/2001
	US2002/0139577A1	10/3/2002	Miller, Charles A.	174/261		3/27/2001
	US2002/0164838A1	Nov-02	Moon et al.	438/107		6/5/2001
	US2002/0180022A1	12/5/2002	Emoto, Yoshiaki	257/686		7/19/2002
	6,072,233	6/6/2000	Corisis et al.	257/686		5/4/1998
	3,436,604	04/01/1969	Hyltin	317/101		04/25/1966
	3,746,934	07/17/1973	Stein	317/101		05/06/1971
	3,772,776	11/20/1973	Weisenburger	29/628		12/03/1969
	4,288,841	09/08/1981	Gogal	361/414		09/20/1979
	4,696,525	09/29/1987	Coller, et al	439/69		12/13/1985
	4,823,234	04/18/1989	Konishi, et al	361/386		07/01/1986
	4,833,568	05/23/1989	Berhold	361/383		01/29/1988
	4,911,643	03/27/1990	Perry, et al	439/67		08/03/1989
	4,953,060	08/28/1990	Lauffer, et al	361/388		05/05/1989
	4,956,694	09/11/1990	Eide	357/74		11/04/1988

**FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION**

Document Number	Date	Country	Class	Subclass	Translation	
					Yes	No

**OTHER DOCUMENTS  
(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)**

	"Tessera Introduces uZ™ - Ball Stacked Memory Package for Computing and Portable Electronic Products" Joyce Smaragdis, Tessera Public Relations; 2 figures that purport to be directed to the uZ™ - Ball Stacked Memory Package.
	Ron Bauer, Intel. "Stacked-CSP Delivers Flexibility, Reliability, and Space-Saving Capabilities", Spring 2002.
	Tessera uZ Ball Stack Package. 4 figures that purport to be directed to the uZ - Ball Stacked Memory, 1 Page.
	"Vertically-Integrated Package," Alvin Weinberg, Pacesetter, Inc. and W. Kinzy Jones, Florida International University

EXAMINER	DATE CONSIDERED
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Form PTO-1449 (Modified) (Rev. 11/92)				U.S. Dept. of Commerce Patent and Trademark Office		ATTY. DOCKET NO.		SERIAL NO.	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)  (37 CFR 1.98(b))				254-094-CIP4/CIP-MB					
				<b>APPLICANT</b> Julian Partridge & James D. Wehrly, Jr.					
				<b>FILING DATE</b> 4/20/2004		<b>GROUP</b>			
<b>U.S. PATENT DOCUMENTS</b>									
*Examiner Initial	Patent Number	Issue Date	Patentee		Class	Subclass	Filing Date (if appropriate)		
	4,983,533	01/08/1991	Go		437/7		10/28/1987		
	5,041,015	08/20/1991	Travis		439/492		03/30/1990		
	5,099,393	03/24/1992	Bentlage, et al		361/413		03/25/1991		
	5,138,430	08/11/1992	Gow, 3 <sup>rd</sup> , et al		357/70		06/06/1991		
	5,159,434	10/27/1992	Kohno, et al		357/80		02/01/1991		
	5,222,014	06/22/1993	Lin		361/414		03/02/1992		
	5,224,023	06/29/1993	Smith, et al.		361/412		02/10/1992		
	5,241,454	08/31/1993	Armeen, et al		361/744		01/22/1992		
	5,247,423	09/21/1993	Lin, et al		361/719		05/26/1992		
	5,259,770	11/09/1993	Bates, et al		439/66		03/19/1992		
	5,281,852	01/25/1994	Normington		257/685		12/10/1991		
	5,313,097	05/17/1994	Haj-Ali-Ahmadi, et al		257/706				
	5,375,041	12/20/1994	McMahon		361/749		12/02/1992		
	5,484,959	01/16/1996	Burns		174/524		12/11/1992		
	5,572,065	11/05/1996	Burns		257/666		10/24/1994		
	5,612,570	03/18/1997	Eide, et al		257/686		04/13/1995		
<b>FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION</b>									
	Document Number	Date	Country	Class	Subclass	Translation			
						Yes	No		
<b>OTHER DOCUMENTS</b> <i>(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)</i>									
<b>EXAMINER</b>				<b>DATE CONSIDERED</b>					
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									

Form PTO-1449 (Modified) (Rev. 11/92)				U.S. Dept. of Commerce Patent and Trademark Office		ATTY. DOCKET NO. 254-094-CIP4/CIP-MB		SERIAL NO.	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)				<b>APPLICANT</b> Julian Partridge & James D. Wehrly, Jr.					
(37 CFR 1.98(b))				<b>FILING DATE</b> 4/20/2004		<b>GROUP</b>			
<b>U.S. PATENT DOCUMENTS</b>									
*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)			
	5,644,161	07/01/1997	Burns	257/668		06/07/1995			
	5,776,797	07/07/1998	Nicewarner, Jr., et al	438/107		07/02/1997			
	5,778,552	7/14/1998	LeGuin	33/559		5/8/1996			
	5,801,437	09/01/1998	Burns	257/685		08/11/1995			
	5,805,422	09/08/1998	Otake, et al	361/749		08/15/1997			
	5,869,353	02/09/1999	Levy, et al	438/109		11/17/1997			
	5,926,369	07/20/1999	Ingraham, et al	361/699		01/22/1998			
	5,949,657	09/07/1999	Karabatsos	361/803		08/25/1998			
	6,014,316	01/11/2000	Eide	361/735					
	6,172,874B1	01/09/2001	Bartilson	361/719		04/06/1998			
	6,208,521B1	3/27/2001	Nakatsuka	361/749		5/19/1998			
	6,225,688B1	05/01/2001	Kim et al.	257/686		02/04/1999			
	6,281,577B1	08/28/2001	Oppermann et al.	257/724		04/22/1997			
	6,392,162B1	05/21/2002	Karabatsos	174/261		11/10/2000			
<b>FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION</b>									
	Document Number	Date	Country	Class	Subclass	Translation			
						Yes	No		
<b>OTHER DOCUMENTS</b> <i>(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)</i>									
EXAMINER				DATE CONSIDERED					
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									

11/